



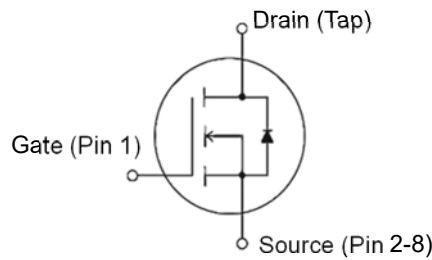
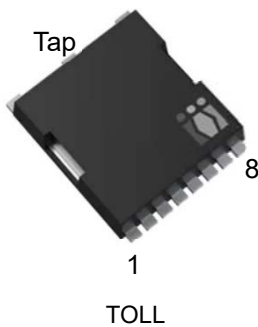
650V N-Channel Power MOSFET

Product Summary

Parameter	Value	Unit
$V_{DS} @ T_{jmax}$	650	V
$R_{DS(on),max} @ V_{GS} = 10\text{ V}$	65	mΩ
$I_D @ V_{GS} = 10\text{ V}$	40	A
P_{tot}	208	W

Features

- * Low on-resistance
- * Low switching losses
- * Excellent FOM
- * Excellent stability and uniformity



Application

- * PC power
- * Server power
- * EV charger
- * LED lighting
- * UPS

Maximum ratings $T_A = 25^\circ\text{C}$ unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain - source voltage	V_{DS}	650	V
Continuous drain current	I_D	$T_C @ 25^\circ\text{C}$	40
		$T_C @ 100^\circ\text{C}$	25
Pulsed drain current t_p limited by T_j max (Note 1)	I_D pulsed	125	A
Single pulse avalanche energy (Note 2)	E_{AS}	700	mJ
Gate-source voltage	V_{GS}	± 30	V
Power dissipation	P_{tot}	208	W
Storage temperature range	T_{STG}	- 55 to +150	$^\circ\text{C}$
Operating junction temperature range	T_j	- 55 to +150	$^\circ\text{C}$



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Electrical characteristics $T_A = 25^\circ\text{C}$ unless otherwise specified						
Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Off characteristics						
Drain-source breakdown voltage	$V_{GS} = 0\text{V}, I_D = 1\text{mA}$	BV_{DSS}	650	---	---	V
Gate-source leakage	$V_{GS} = \pm 30\text{V}, V_{DS}=0\text{V}$	I_{GSS}	---	---	± 1	μA
Zero gate voltage drain current	$V_{DS}= 650\text{V}, V_{GS}= 0\text{V}, T_J=25^\circ\text{C}$	I_{DSS}	---	---	5	μA
On characteristics						
Drain-source on-state resistance	$V_{GS} = 10\text{V}, I_D = 20\text{A}, T_J=25^\circ\text{C}$	$R_{DS(on)}$	---	58	65	$\text{m}\Omega$
Gate-source threshold voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(th)}$	3	4	5	V
Gate resistance	$f = 1\text{MHz}, \text{open drain}$	R_G	---	6	---	Ω
Dynamic and switching characteristics-						
Gate-source charge	$V_{DD} = 400\text{V}, I_D = 20\text{A}$ $V_{GS} = 0 \text{ to } 10\text{V}$	Q_{gs}	---	27	---	nC
Gate-drain charge		Q_{gd}	---	31	---	
Gate charge total		Q_g	---	81	---	
Turn-on delay time	$V_{DD} = 400\text{V}, I_D = 20\text{A}$ $V_{GS} = 10\text{V}, R_G = 5\Omega$	$t_{d(on)}$	---	23	---	ns
Rise time		t_r	---	14	---	
Turn-off delay time		$t_{d(off)}$	---	98	---	
Fall time		t_f	---	18	---	
Input capacitance	$V_{GS} = 0\text{V}, V_{DS} = 50\text{V},$ $f = 1\text{MHz}$	C_{iss}	---	3900	---	pF
Output capacitance		C_{oss}	---	130	---	

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Drain-source diode characteristics and maximum ratings						
Inverse diode forward voltage	$I_S = 40\text{A}, V_{GS} = 0\text{V}$	V_{SD}	---	0.95	1.4	V
Reverse recovery time	$V_R = 400\text{V}, I_F = 20\text{A},$ $di_F / dt = 100\text{A} / \mu\text{S}$	t_{rr}	---	155	---	ns
Reverse recovery charge		Q_{rr}	---	1.1	---	μC

Notes:

1. Repetitive rating : pulsed width limited by maximum junction temperature.
2. $V_{DD}=50\text{V}$, starting $T_J=25^\circ\text{C}$.



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Thermal characteristics			
Thermal resistance junction-to-case	R _{thJC}	0.6	°C / W
Thermal resistance junction-to-ambient	R _{thJA}	40	

Package Marking and Ordering Information

Type / Ordering Code	Package	Packaging	Related Links
I3JL40N65QS	TOLL	Tape & Reel	see Package Outline

Electrical Characteristics Diagrams

Fig 1: Power dissipation

$P_{tot} = f(T_c)$

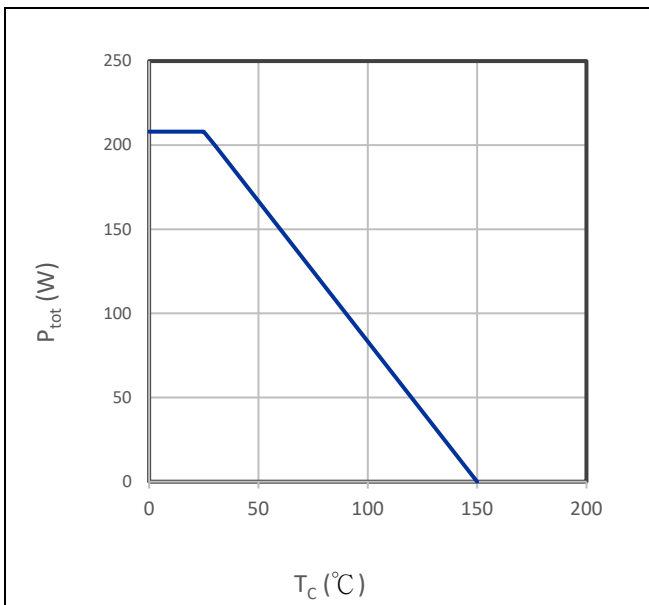
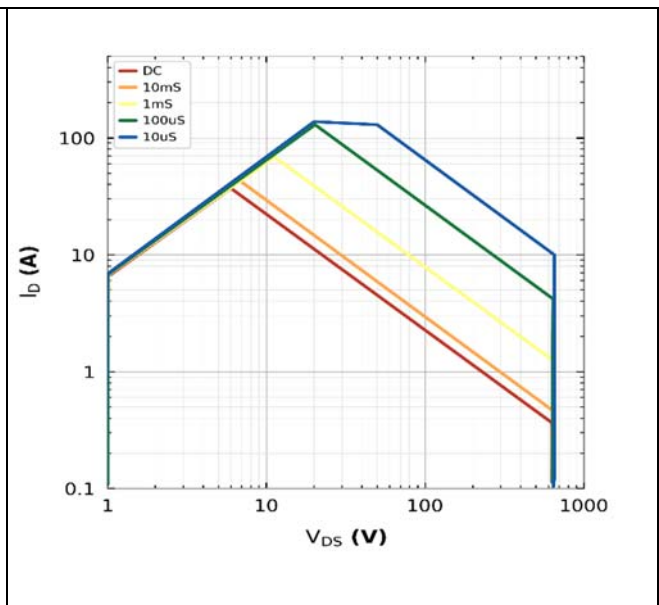


Fig 2: Safe operating area

$I_D = f(V_{DS}) ; V_{GS} > 10V, D = 0, T_c = 25^\circ C ; \text{parameter : } t_p$





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Fig 3: Typ. transfer characteristics

$I_D = f(V_{GS})$; $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$, $T_j = 25^\circ C$

Fig 4: Typ. output characteristics

$I_D = f(V_{DS})$; $T_j = 25^\circ C$; parameter: V_{GS}

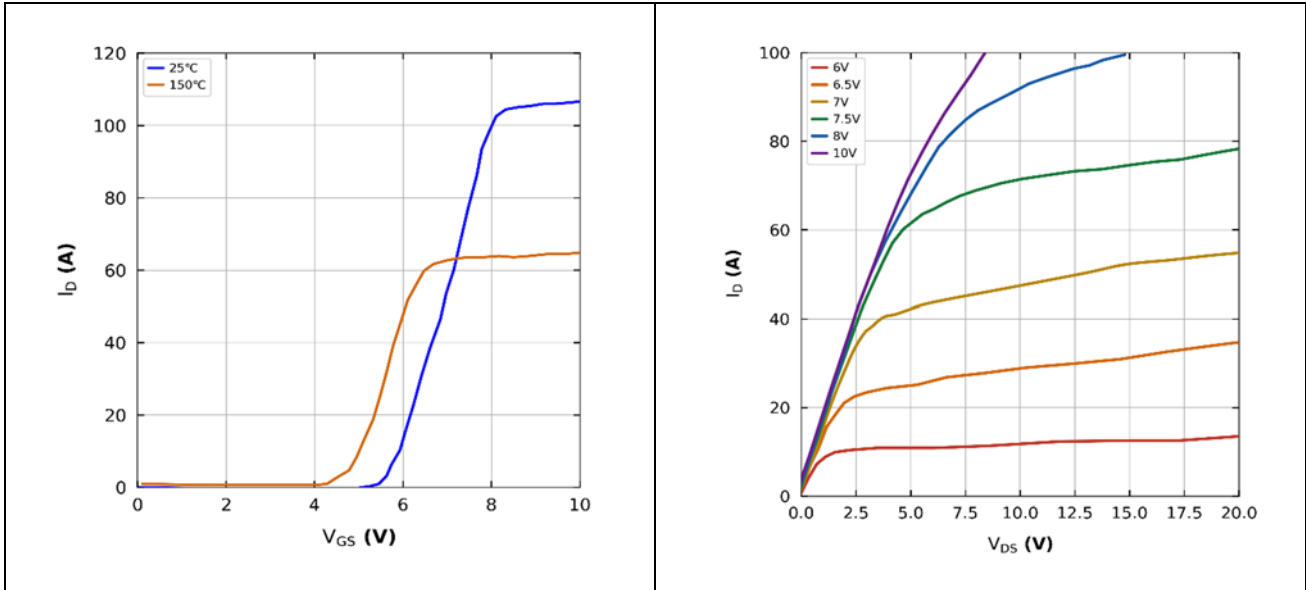
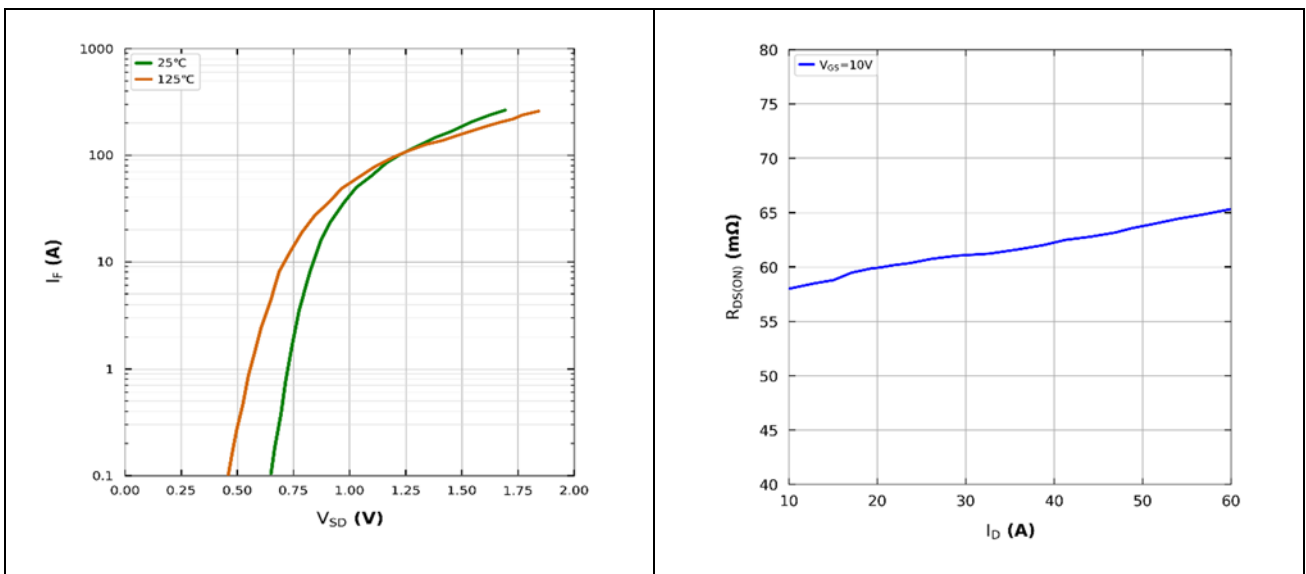


Fig 5: Forward characteristics of body diode

$I_F = f(V_{SD})$; parameter: T_j

Fig 6: Typ. drain source on-resistance

$R_{DS(on)} = f(I_D)$; $T_j = 25^\circ C$; parameter: V_{GS}





I3JL40N65QS

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Fig 7: Drain-source on-state resistance

$R_{DS(on)} = f(T_J)$; $I_D = 21A$, $V_{GS} = 10V$

Fig 8: Typ. capacitances

$C = f(V_{DS})$; parameter: $V_{GS} = 0V$, $f = 1MHz$

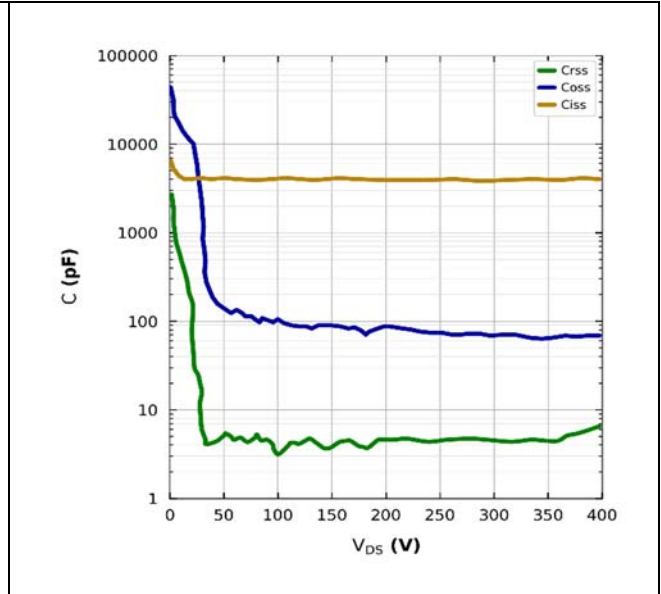
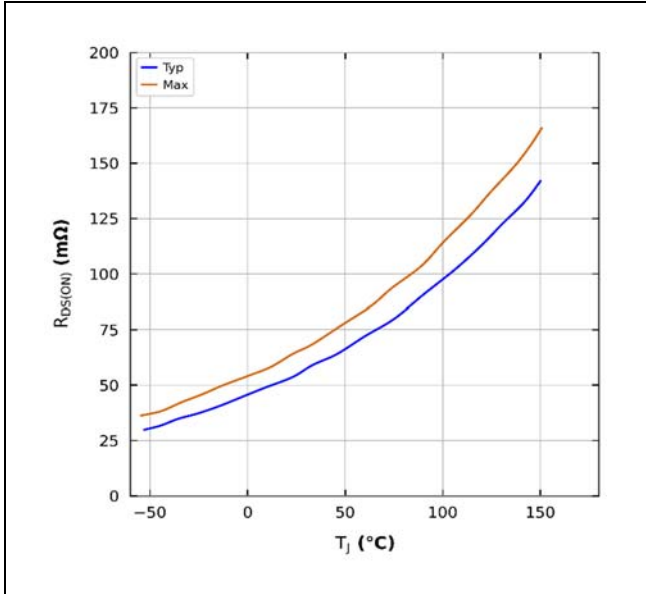
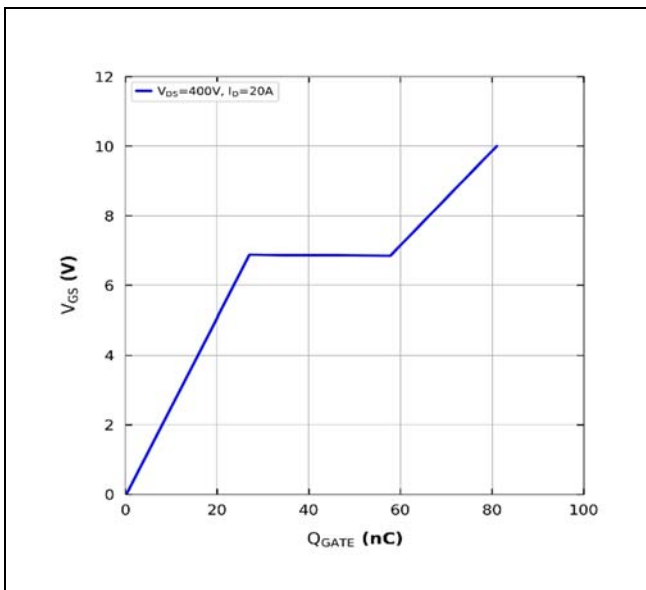


Fig 9: Typ. gate charge

$V_{GS} = f(Q_{GATE})$; $I_D = 20A$ pulsed





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Test Circuit

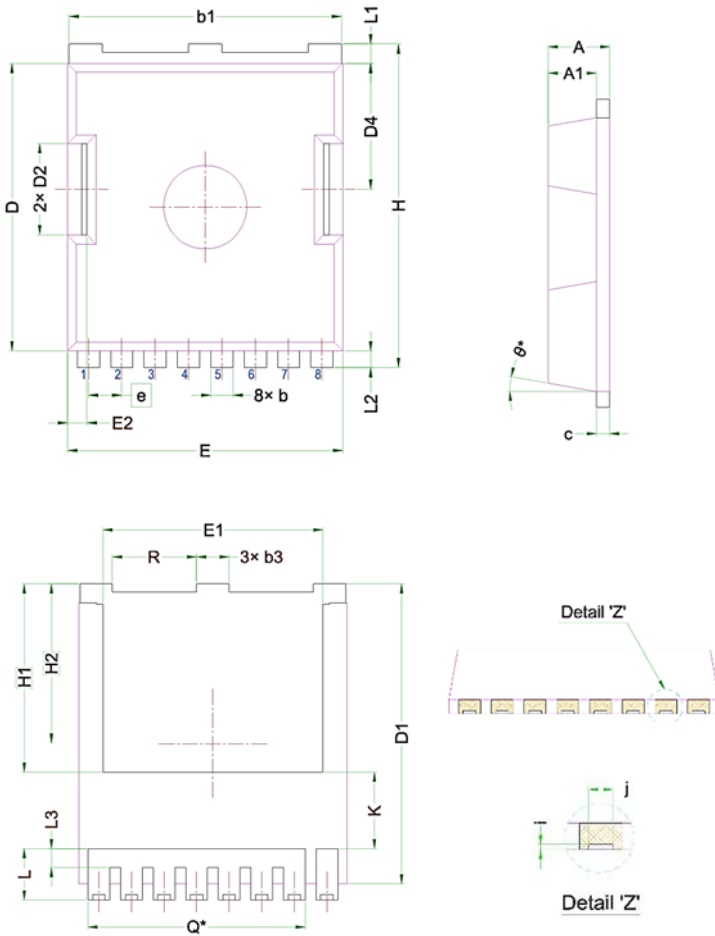
<p>Unclamped test circuit</p>	<p>Unclamped test waveform</p>
<p>Gate charge test circuit</p> <p> V_{GS} $10V$ Pulse width $\leq 1\mu s$ Duty factor $\leq 0.1\%$ </p>	<p>Basic gate charge waveform</p> <p>Charge \longrightarrow</p>
<p>Diode recovery test circuit</p> <p>Current regulator Same type as D.U.T.</p> <p> $12V$ $50k\Omega$ $2\mu F$ $0.3\mu F$ </p> <p> V_{GS} $3mA$ I_o </p> <p>Current sampling resistors</p>	<p>Diode recovery test waveform</p> <p> $Q_{rr} = -\int Idt$ </p>
<p>Switching test circuit (resistor load)</p>	<p>Switching test waveform</p>



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Package Outline



Ref.	Dimensions		
	Min.	Typ.	Max.
A	2.20	2.30	2.40
A1	1.70	1.80	1.90
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b3	1.10	1.20	1.30
c	0.40	0.50	0.60
D	10.28	10.38	10.48
D1	10.98	11.08	11.18
D2	3.20	3.30	3.40
D4	4.45	4.55	4.65
E	9.80	9.90	10.00
E1	8.00	8.10	8.20
E2	0.60	0.70	0.80
e	1.20 BSC		
H	11.58	11.68	11.78
H1	6.95 BSC		
H2	5.89 BSC		
i	0.10 REF.		
j	0.46 REF.		
K	2.80 REF.		
L	1.40	1.90	2.10
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L3	0.30	0.70	0.80
N	8		
Q	8.00 REF.		
R	3.00	3.10	3.20
θ	10° REF.		