



650V N-Channel Power MOSFET

Product Summary

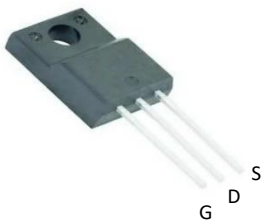
Parameter	Value	Unit
$V_{DS} @ T_{jmax}$	650	V
$R_{DS(on),max} @ V_{GS} = 10\text{ V}$	120	mΩ
$I_D @ V_{GS} = 10\text{ V}$	30	A
P_{tot}	45	W

Features

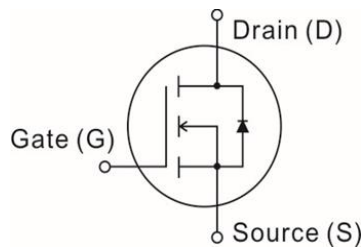
- * Low on-resistance
- * Low switching losses
- * Excellent FOM
- * excellent stability and uniformity

Application

- * PC power
- * Server power
- * EV charger
- * LED lighting
- * UPS



ITO-220



Maximum ratings $T_A = 25^\circ\text{C}$ unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain - source voltage	V_{DS}	650	V
Continuous drain current	I_D	$T_C @ 25^\circ\text{C}$	30
		$T_C @ 100^\circ\text{C}$	19
Pulsed drain current t_p limited by T_j max (Note 1)	$I_{D\text{ pulsed}}$	90	A
Single pulse avalanche energy (Note 2)	E_{AS}	850	mJ
Gate-source voltage	V_{GS}	± 30	V
Power dissipation	P_{tot}	45	W
Storage temperature range	T_{STG}	- 55 to +150	$^\circ\text{C}$
Operating junction temperature range	T_j	- 55 to +150	$^\circ\text{C}$



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Electrical characteristics $T_A = 25^\circ\text{C}$ unless otherwise specified						
Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Off characteristics						
Drain-source breakdown voltage	$V_{GS} = 0V, I_D = 1mA$	BV_{DSS}	650	---	---	V
Gate-source leakage	$V_{GS} = \pm 30V, V_{DS}=0V$	I_{GSS}	---	---	± 100	nA
Zero gate voltage drain current	$V_{DS}= 650V, V_{GS}= 0V, T_j=25^\circ\text{C}$	I_{DSS}	---	---	5	μA
On characteristics						
Drain-source on-state resistance	$V_{GS} = 10V, I_D = 15A, T_j=25^\circ\text{C}$	$R_{DS(on)}$	---	105	120	$\text{m}\Omega$
Gate-source threshold voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(th)}$	2.5	3.8	4.5	V
Gate resistance	$f = 1\text{MHz}, \text{open drain}$	R_G	---	12.5	---	Ω
Dynamic and switching characteristics-						
Gate-source charge	$V_{DD} = 400V, I_D = 15A$ $V_{GS} = 0 \text{ to } 10V$	Q_{gs}	---	19	---	nC
Gate-drain charge		Q_{gd}	---	20	---	
Gate charge total		Q_g	---	58	---	
Turn-on delay time	$V_{DD} = 400V, I_D = 15A$ $V_{GS} = 10V, R_G = 2\Omega$	$t_{d(on)}$	---	31	---	ns
Rise time		t_r	---	60	---	
Turn-off delay time		$t_{d(off)}$	---	102	---	
Fall time		t_f	---	58	---	
Input capacitance	$V_{GS} = 0V, V_{DS} = 100V,$ $f = 1\text{MHz}$	C_{iss}	---	2500	---	pF
Output capacitance		C_{oss}	---	100	---	

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Drain-source diode characteristics and maximum ratings						
Inverse diode forward voltage	$I_S = 15A, V_{GS} = 0V$	V_{SD}	---	0.9	---	V
Reverse recovery time	$V_R = 400V, I_F = 15A,$ $di_F / dt = 100A / \mu\text{S}$	t_{rr}	---	130	---	ns
Reverse recovery charge		Q_{rr}	---	0.85	---	μC

Notes:

1. Repetitive rating : pulsed width limited by maximum junction temperature.
2. $V_{DD}=50V$, starting $T_j=25^\circ\text{C}$.



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Thermal characteristics			
Thermal resistance junction-to-case	R _{thJC}	2.8	°C / W
Thermal resistance junction-to-ambient	R _{thJA}	80	

Package Marking and Ordering Information

Type / Ordering Code	Package	Packaging	Related Links
I3JF30N65Q	TO-220 fullPAK	Tube	see Package Outline

Electrical Characteristics Diagrams

Fig 1: Power dissipation

$P_{tot} = f(T_c)$

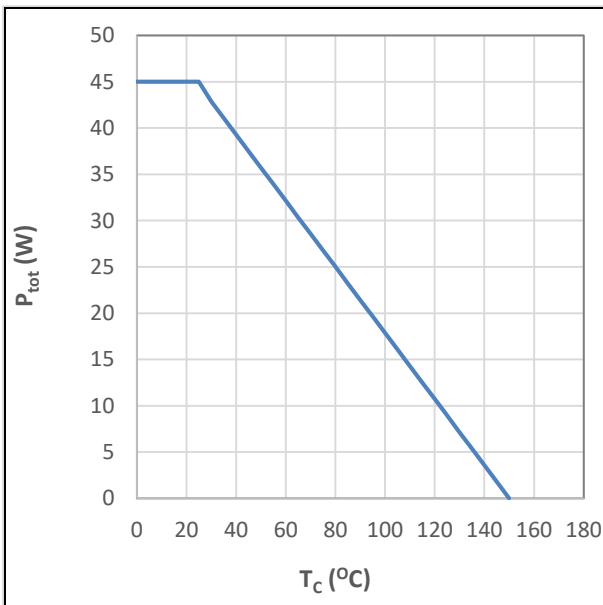
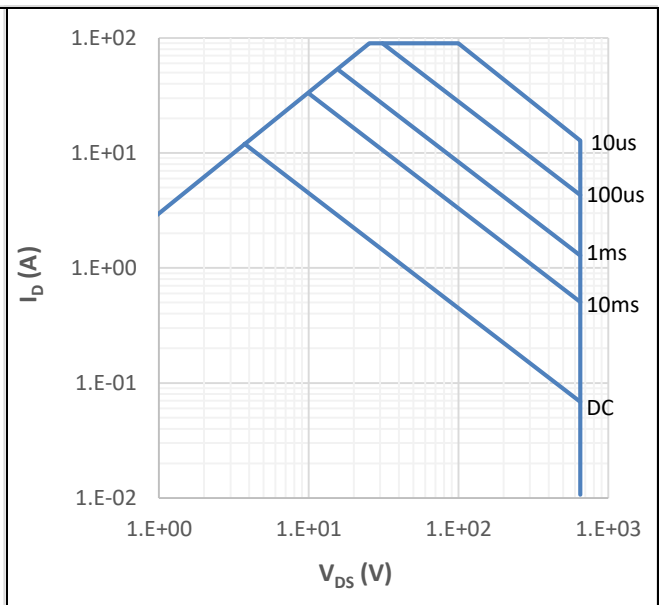


Fig 2: Safe operating area

$I_D = f(V_{DS}) ; V_{GS} > 10V, D = 0, T_c = 25°C ; \text{parameter : } t_p$





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Fig 3: Typ. transfer characteristics

$I_D = f(V_{GS})$; $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$, $T_j = 25^\circ C$

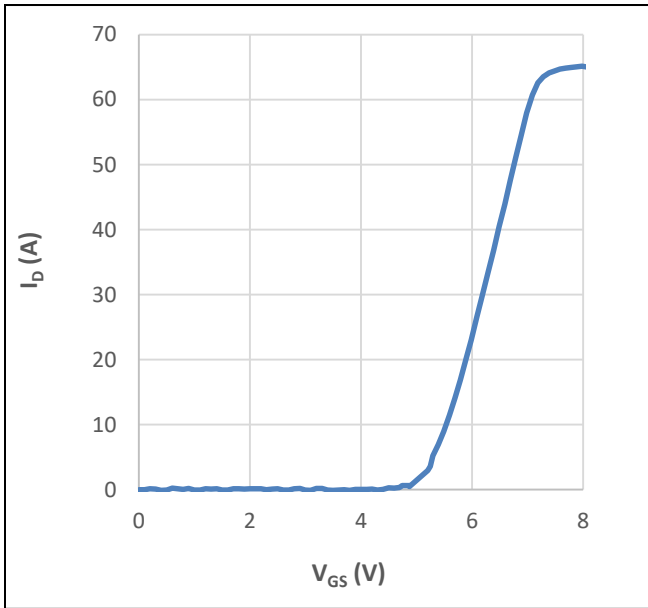


Fig 4: Typ. output characteristics

$I_D = f(V_{DS})$; $T_j = 25^\circ C$; parameter: V_{GS}

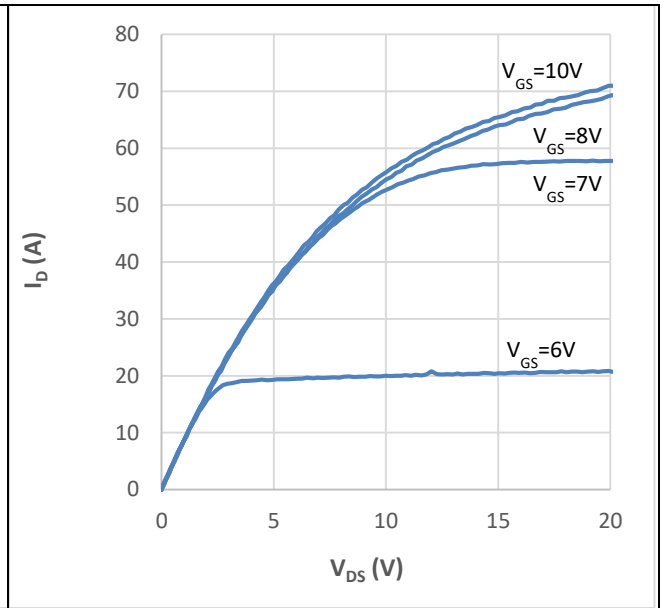


Fig 5: Forward characteristics of body diode

$I_F = f(V_{SD})$; parameter: T_j

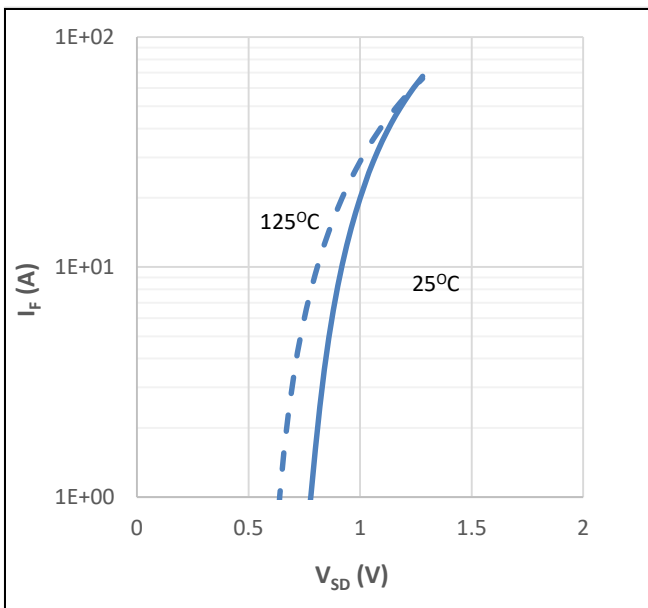
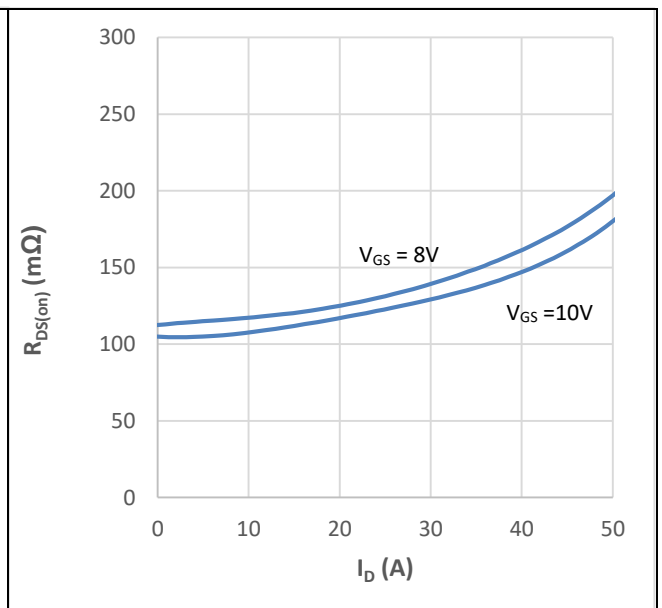


Fig 6: Typ. drain source on-resistance

$R_{DS(on)} = f(I_D)$; $T_j = 25^\circ C$; parameter: V_{GS}





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Fig 7: Drain-source on-state resistance

$R_{DS(on)} = f(T_j)$; $I_D = 15A$, $V_{GS} = 10V$

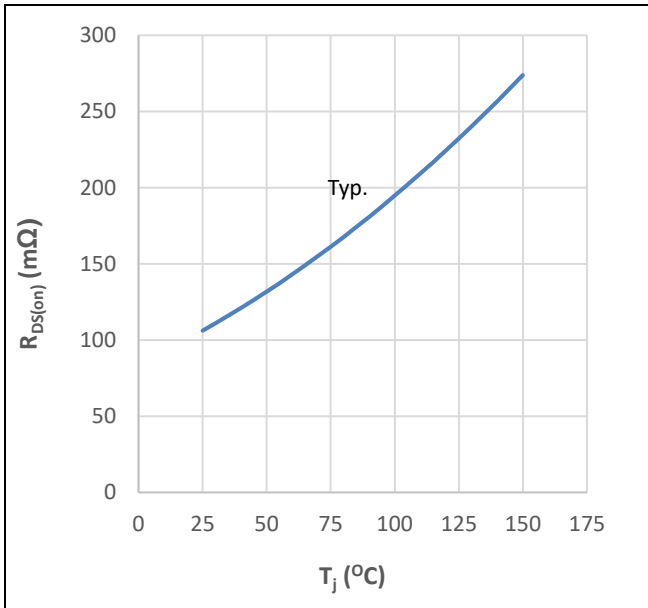


Fig 8: Typ. capacitances

$C = f(V_{DS})$; parameter : $V_{GS} = 0V$, $f = 1MHz$

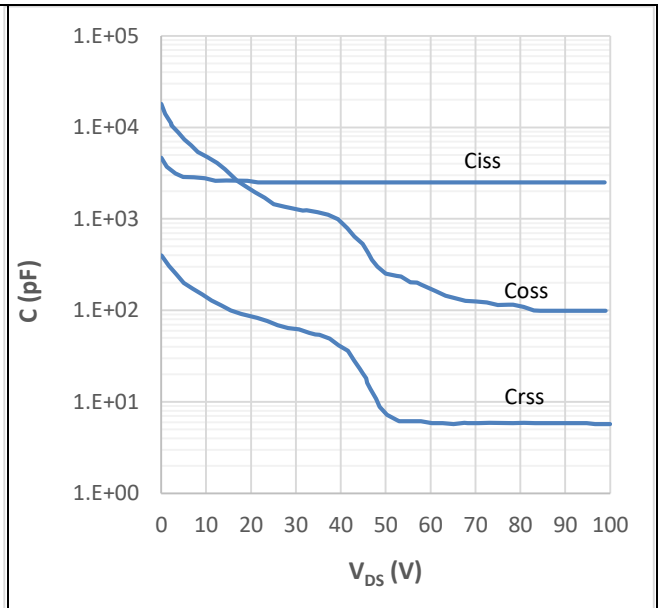
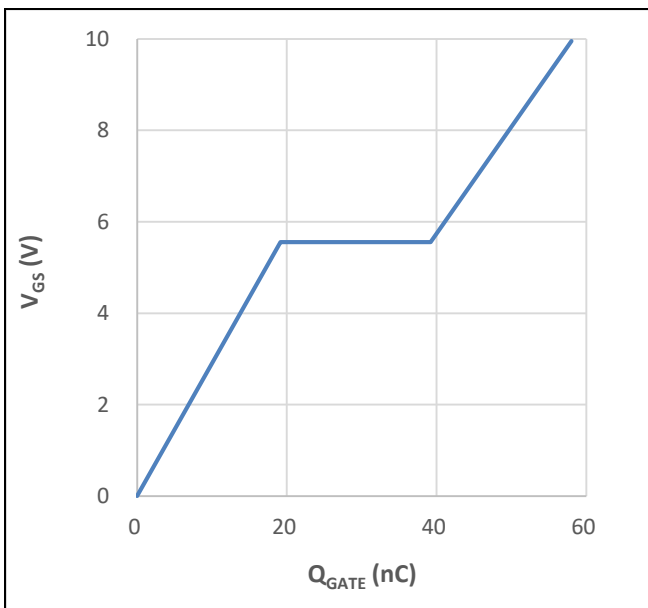


Fig 9: Typ. gate charge

$V_{GS} = f(Q_{GATE})$; $I_D = 15A$ pulsed





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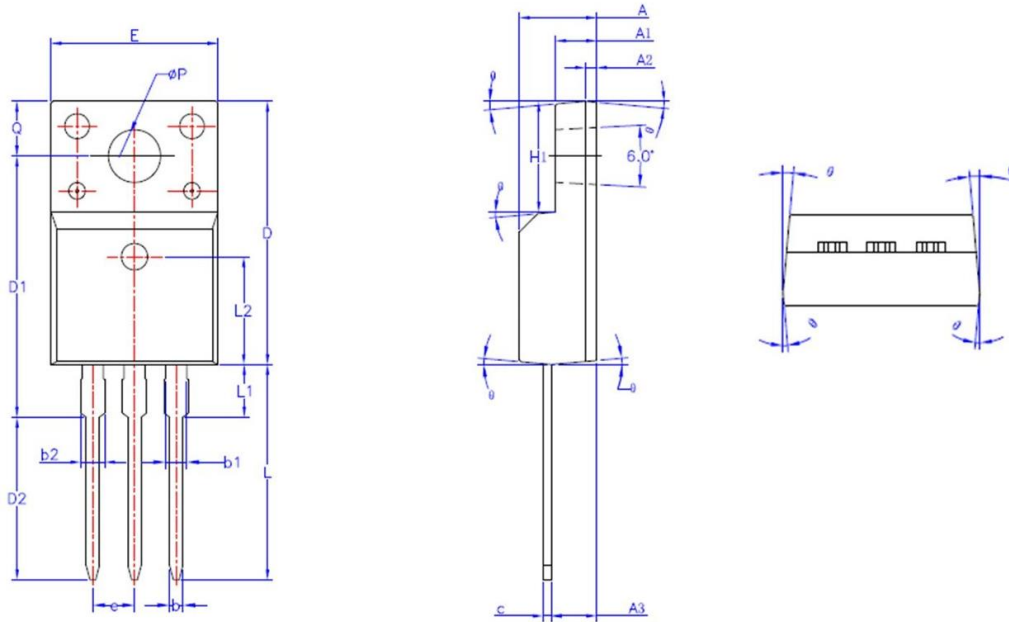
Test Circuit

<p>Unclamped test circuit</p>	<p>Unclamped test waveform</p>
<p>Gate charge test circuit</p>	<p>Basic gate charge waveform</p>
<p>Diode recovery test circuit</p>	<p>Diode recovery test waveform</p>
<p>Switching test circuit (resistor load)</p>	<p>Switching test waveform</p>



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Package Outline



SYMBOL	MIN	NOM	MAX
A	4.50	4.70	4.83
A1	2.34	2.54	2.74
A2	0.7REF		
A3	2.56	2.76	2.93
b	0.70	--	0.90
b1	1.18	--	1.40
b2	--	--	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	9.60	9.80	10.00
E	9.96	10.16	10.36
e	2.54BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	-	-	3.50
L2	6.50REF		
ϕP	3.08	3.18	3.28
Q	3.20	-	3.40
$\theta 1$	1°	3°	5°